



International SOI TF

July 10, 2017

San Francisco Marriott Marquis, San Francisco, CA

9:00 – 10:00 AM

AGENDA

	Time
1 Welcome/Call to Order	09:00
1.1 Introductions	
1.2 Required Elements (Membership Requirements, Antitrust and Intellectual Property Reminders, and Effective Meeting Guidelines)	
1.3 Agenda Review	
2 Review of Previous Meeting Minutes	09:05
3 New Business	09:10
3.1 Presentation: “SOI Substrate Engineering for FDSOI Devices” C. Maleville, SOITEC, Bernin, France	
3.2 Presentation: “Materials and Characterization Issues in High Resistivity SOI for RF Applications” R. Standley, Global Wafers, St Peters, MO, USA	
4 Action Item Review	09:55
4.1 Open Action Items	
4.2 New Action Items	
5 Next Meeting and Adjournment	10:00
5.1 The next SOI TF meeting is scheduled for April '18 at the North America Standards Spring Meeting	